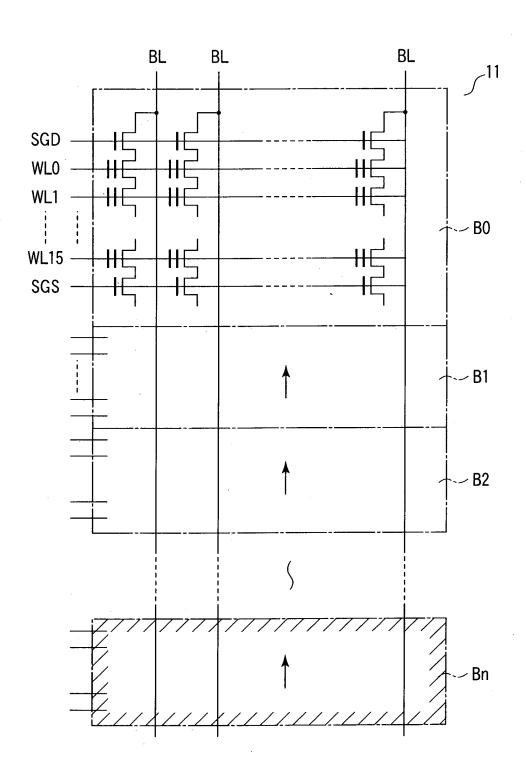
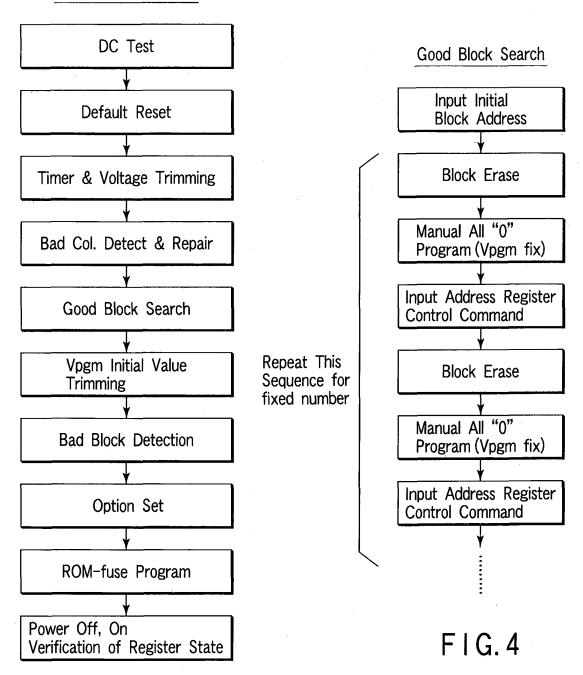


FIG.1



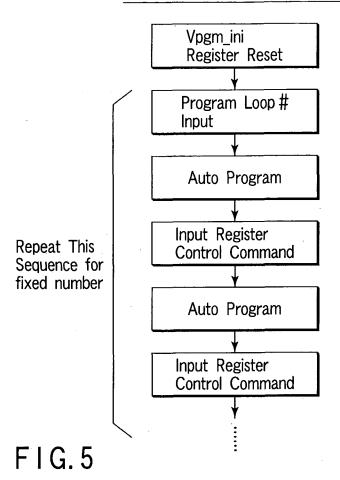
F1G.2

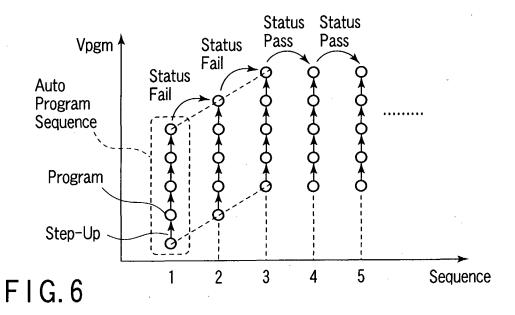
Wafer Test Sequence

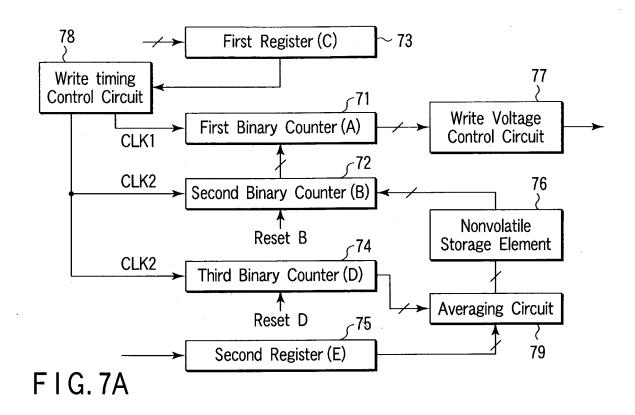


F I G. 3

Vpgm Initial Value Determination







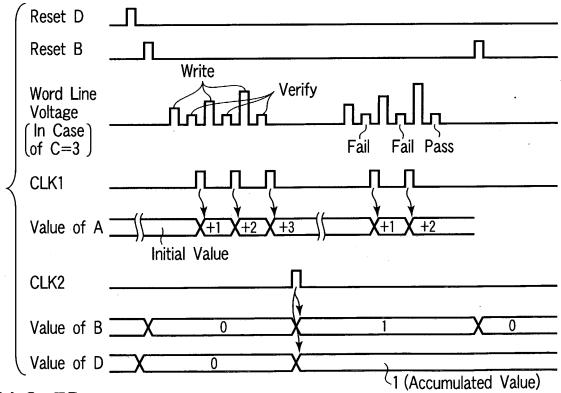
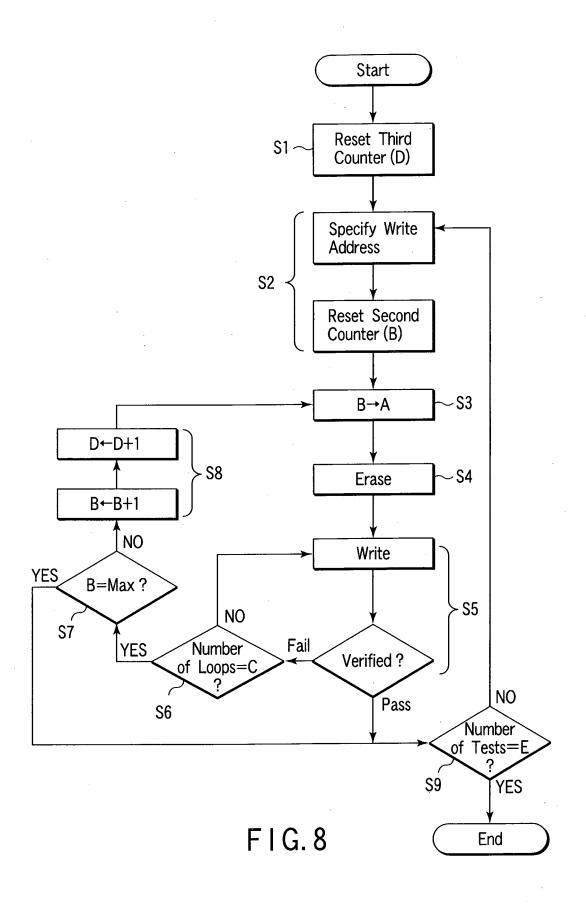
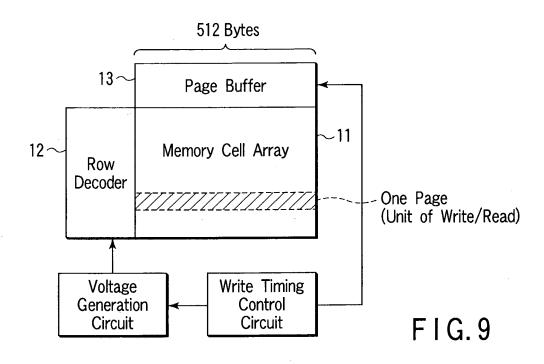
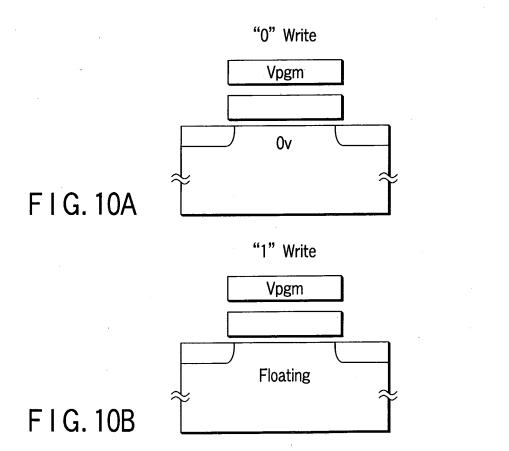
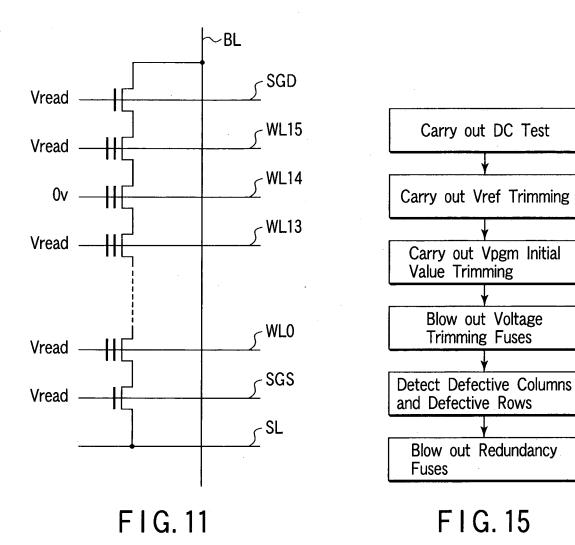


FIG.7B









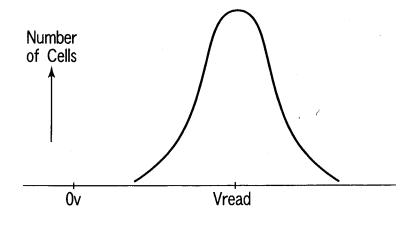
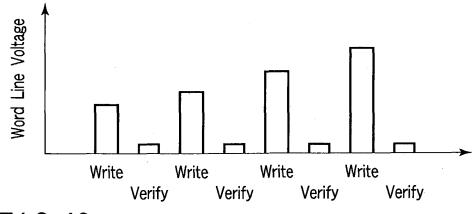


FIG. 12



F I G. 13

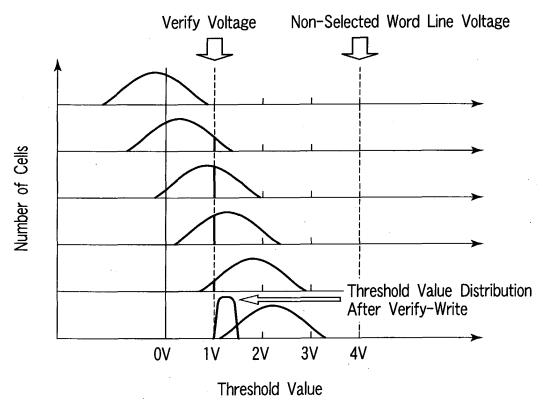


FIG. 14